



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

ATTN: Application Branch

Noriko SHINOMIYA

Serial No.: 10/606,283

Filed: June 26, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING APPARATUS,
SEMICONDUCTOR INTEGRATED CIRCUIT DESIGNING METHOD, SEMICONDUCTOR
INTEGRATED CIRCUIT MANUFACTURING METHOD, AND READABLE RECORDING
MEDIA

REQUEST FOR FILING RECEIPT

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Please forward the Official Filing Receipt for the above-
identified matter to us as soon as possible.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

Date: January 26, 2004
Attorney Docket No. HYAE:165
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